

APPLICATION DATA SHEET

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Title of Invention: ON CHIP TIMING ADJUSTMENT IN MULTI-CHANNEL FAST DATA TRANSFER

Legal Representative:

Attorney or Agent: Mr. Richard A. Henkler Esq.

Registration Number: 39220

Customer Number Correspondence Address: 28211



INVENTOR(s):

Primary Citizenship: Canada

Given Name: Kai

Middle Name: Di

Family Name: Feng

Residence City: Essex Junction

Residence State: Vermont

Residence Country: US

Address: 30 Fairview Drive

Essex Junction Vermont, 05452 US